



技术指标:

1. 温度范围: $-25^{\circ}\text{C} \sim +85^{\circ}\text{C}$.
2. 额定电压: 250V AC, DC.
3. 耐电压: 800V AC/minute.
4. 接触电阻: $\leq 20\text{m}\Omega$.
5. 绝缘电阻: $\geq 1000\text{M}\Omega$.
6. 额定电流: 3A AC, DC.

型号	CKT	A	B
WAFER-PHD2.0-4PJK-GW12	2*2	2.00	6.00
WAFER-PHD2.0-6PJK-GW12	2*3	4.00	8.00
WAFER-PHD2.0-8PJK-GW12	2*4	6.00	10.00
WAFER-PHD2.0-10PJK-GW12	2*5	8.00	12.00
WAFER-PHD2.0-12PJK-GW12	2*6	10.00	14.00
WAFER-PHD2.0-14PJK-GW12	2*7	12.00	16.00
WAFER-PHD2.0-16PJK-GW12	2*8	14.00	18.00
WAFER-PHD2.0-18PJK-GW12	2*9	16.00	20.00
WAFER-PHD2.0-20PJK-GW12	2*10	18.00	22.00
WAFER-PHD2.0-22PJK-GW12	2*11	20.00	24.00
WAFER-PHD2.0-24PJK-GW12	2*12	22.00	26.00
WAFER-PHD2.0-26PJK-GW12	2*13	24.00	28.00
WAFER-PHD2.0-28PJK-GW12	2*14	26.00	30.00
WAFER-PHD2.0-30PJK-GW12	2*15	28.00	32.00
WAFER-PHD2.0-32PJK-GW12	2*16	30.00	34.00
WAFER-PHD2.0-34PJK-GW12	2*17	32.00	36.00
WAFER-PHD2.0-36PJK-GW12	2*18	34.00	38.00
WAFER-PHD2.0-38PJK-GW12	2*19	36.00	40.00
WAFER-PHD2.0-40PJK-GW12	2*20	38.00	42.00

MANUFACTURE DWG		东莞市讯普电子科技有限公司 DongGuan XunPu Electronics Co., Ltd	
UNLESS OTHERWISE SPECIFIED TOLERANCES			TITLE: PHD2.0公头封装
DECIMALS:	ANGLES:		PAR WAFER-PHD2.0-NPJK-GW12
X.X ± 0.30	X.* ± 5°		DWN
X.XX ± 0.25	X.X* ± 1°		CHKD
X.XXX ± 0.15		APVD	SCALE: A UNIT: MM
CUSTOMER COPY		SIZE: A4	UNIT: MM
		SHEET: 1F1	REV: A