



PCB Layout Diagram
Top View

NOTE:
 1.MATERIAL:
 a.HOUSING:PBT BLACK UL94V-0
 b.CONTACT:PHOSPHOR BRONZE
 (SURFACE PLATING:Sn 100u"-120u")
 INFORMATION
 2.ELECTRIC:
 a.CONTACT RESISTANCE: 50 MILLIOHMS
 MAX
 b.INSULATION RESISTANCE: 100 MEGA
 OHMS MIN
 c.DIELECTNIC VOLTAGE: 500V AC ONE
 MINUTE
 d.OPERATING TEMPERATURE: -25°C~
 +85°C

3	TERMINAL(-)	1	PHOSPHOR BRONZE, T=0.20mm	Ni(30u")+Sn(80u")-Plating
2	TERMINAL(+)	1	PHOSPHOR BRONZE, T=0.40mm	Ni(30u")+Sn(80u")-Plating
1	HOUSING	1	PBT UL94V-0	BLACK
NO.	PART NAME	Q'TY	MATERIAL	PLATING & COLOR

MANUFACTURE DWG		东莞市讯普电子科技有限公司 DongGuan XunPu Electronics Co.,Ltd	
UNLESS OTHERWISE SPECIFIED TOLERANCES		TITLE: 2.0 Sn DIP	
DECIMALS: X.X±0.30		PAR BS-0201-DK-0B	
ANGLES: X.*±5°		DWN	
X.XX±0.25		CHKD	
X.XXX±0.15		APVD	
CUSTOMER COPY		SCALE N:A UNIT:MM	
		SIZE:A4 SHEET:1F1	
		REV:A	